

Pretty agile and Easy Bring-up ProtoEmulation Function and Hybrid Verification



VeriTiger-K115

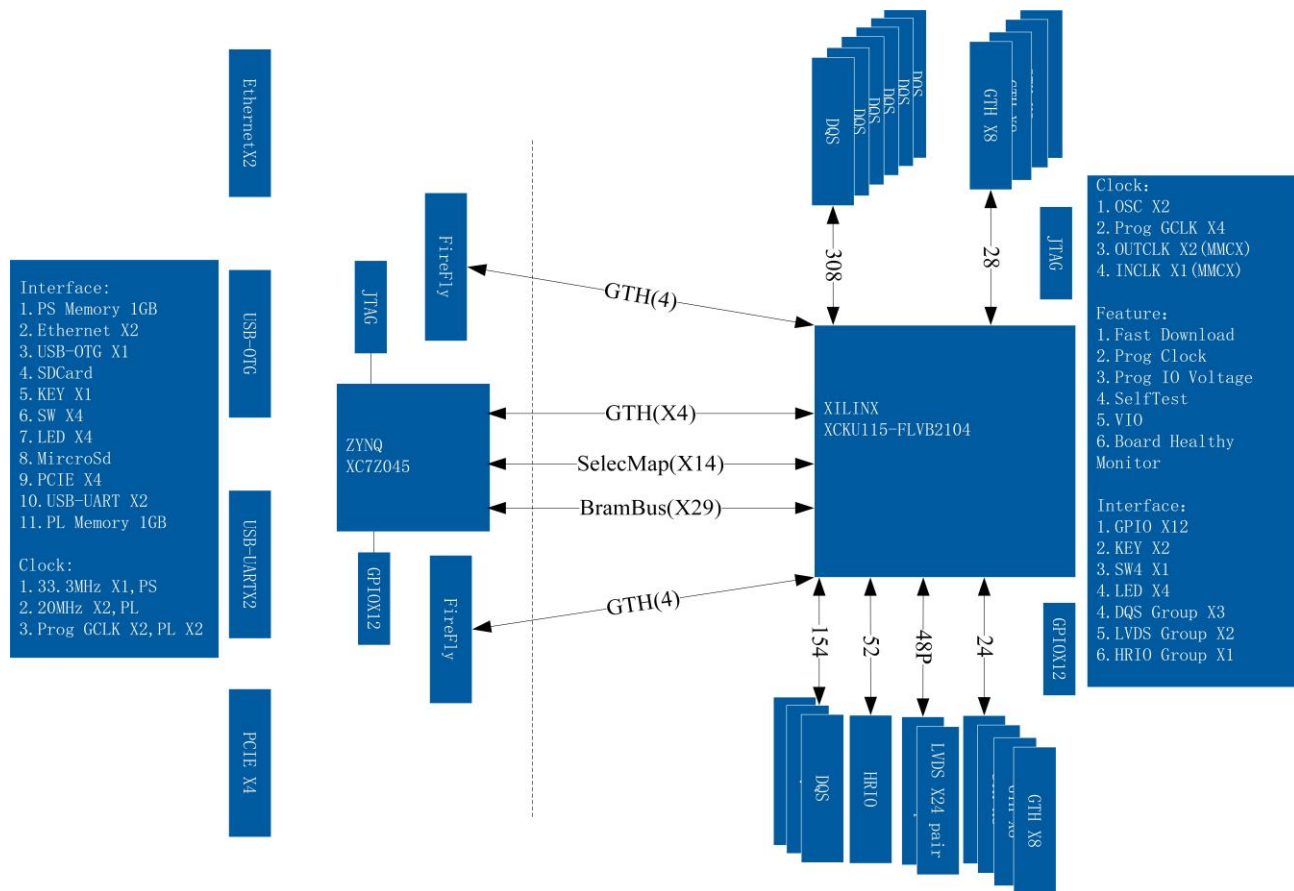
Prototyping for AI, SSD, NB-IoT, Multi-MCU, Block Chain etc



VeriTiger Series FPGA prototyping system provides Rapid Standard Prototyping Platform for various types of digital circuit design verification, significantly reduced development time.

Key Features

- Xilinx XCKU115-FLVB2104 Based Prototyping(Compatibility by XCVU190,VU9P,VU13P).
- Multi Mbank, Support 3 suits DDR3/4 Extended Memory.
- Separate extension interface, in line with HSPI2(High Speed Prototyping Interface2), Provide long-term expandability, up to 622 GPIOs.
- Extension interface with HSPI2-GTH(High Speed Prototyping Interface2-Transceiver), Support PCIe Gen3; SATA;SFP+;Rapid I/O etc.
- Global input clock:2* OSC; 4* Programmable clock;1*M-MCM.
- FPGA Configuration Methods: USB; Remote Ethernet Configuration; SD Card and JTAG.
- Automatic Voltage/Current/Temperature Monitor.
- Smart Fan speed Control, Super sound-off.
- Stand ATX PCIEX6 Con 12V, Max 120W.
- ISO9000 Standard Assembly.
- Build-in power up Selftest include voltage, CLK and IO-level.
- ZYNQ PCIe X4, DU Ethernet interface, DU Sdcard, USB-OTG and UART interface For user custom fuction.
- Support for high-speed interconnections and multiplexing between user FPGAs.
- ProtoWizard v18 Support Windows and Linux OS.



Daughter Card

- DDR4 SODIMM Daughter Card
- LPDDR/LPDDR2/LPDDR3 Daughter Card
- NOR FLASH Daughter Card
- NAND FLASH Daughter Card
- UART Daughter Card
- Analog video input and output Daughter Card
- Analog audio input and output Daughter Card
- HDMI & DVI video input Daughter Card
- LCD Interface
- CCD/CMOS imaging sensors
- DVI Input / Output Daughter Card
- 100M/1000M Ethernet PHY
- USB 1.1/2.0/OTG PHY
- USB Device Interface
- PS2 HID Interface
- TS stream input and output Interface
- Mobile-DDR Daughter Card

Product Properties Sheet

	VeriTiger-K115	VeriTiger-DK115	VeriTiger-QK115
Number of FPGAs	1	2	4
ASIC Gate Capacity	10M ~ 15M	20M ~ 30M	40M ~ 60M
ASIC Gate Capacity	HSPI2-MBank *9	HSPI2-MBank *18	HSPI2-MBank *36
	HSPI2-HRIO *1	HSPI2-HRIO *2	HSPI2-HRIO *4
	HSPI2-LVDS *2	HSPI2-LVDS *4	HSPI2-LVDS *8
	HSPI2-GTH *8	HSPI2-GTH *16	HSPI2-GTH *32
User Accessible I/O	HSPI2-MBank 462	HSPI2-MBank 924	HSPI2-MBank 1848
	HSPI2-HRIO 52 (52*1)	HSPI2-HRIO 52 (52*1)	HSPI2-HRIO 52 (52*1)
	HSPI2-LVDS 48pairs (24*2)	HSPI2-LVDS 48pairs (24*2)	HSPI2-LVDS 48pairs (24*2)
	GPIO[1.8V,12]	GPIO[1.8V,24]	GPIO[1.8V,48]
	KEY*2,SW4*1,LED*4	KEY*4,SW4*2,LED*8	KEY*8,SW4*4,LED*16
Transceivers	52 lane	104 lane	208 lane
FPGA Type	Xilinx XCKU115-FLVB2104(Compatibility by XCVU190,VU9P,VU13P)		
	System Logic Cells [1,451,100]		
	CLB Flip-Flops [1,326,720]		
	Block RAM [75.9Mb]		
	DSP Slice [5,520]		
	PCIe Gen3*8 [6]		
	GTH 16.3 Transceivers [60]		
Clock Resources	OSC *2	OSC *4	OSC *8
	Programmable clock *4	Programmable clock *8	Programmable clock *16
	Input MMCX clock *1	Input MMCX clock *2	Input MMCX clock *4
	Output MMCX clock *2	Output MMCX clock *4	Output MMCX clock *8
Memory	DDR3/64bit/8GByte *3	DDR3/64bit/8GByte *6	DDR3/64bit/8GByte *12
	DDR4/64bit/8GByte *3	DDR4/64bit/8GByte *6	DDR4/64bit/8GByte *12
Configuration	Ethernet Download		
	USB Download		
	SD Card Download		
	JTAG		
Powers	Stand ATX PCIeX6 Con 12V,Max 120W	Stand ATX PCIeX6 Con 12V,Max 240W	Stand ATX PCIeX6 Con 12V, Max 480W
Daughter Boards	AutoDetect;ALL HSPI2 Interface		
ProWizard Support	Windows, Linux		
Size(mm)	244*220*63	244*440*63	488*440*63

Ordering information

Product Name	No. of FPGA	FPGA Part
VeriTiger-K115	1	Kintex Ultrascale XCKU115-FLVB2104
VeriTiger-DK115	2	Kintex Ultrascale XCKU115-FLVB2104
VeriTiger-QK115	4	Kintex Ultrascale XCKU115-FLVB2104